

Chapter 1 : Solution manual for Microelectronic Circuits Sedra Smith 7th Edition : ElectricalEngineering

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Which is the MSB? What is the change in i_O resulting from the LSB changing from 0 to 1? Each sample is represented by 16 bits. What is the speed of this system in bits per second? For each, find the voltage, current, and power gains A_v , A_i , and A_p , respectively both as ratios and in dB: The average current in each supply is measured to be 20 mA. Find the voltage gain, current gain, and power gain expressed as ratios and in decibels as well as the supply power, amplifier dissipation, and amplifier efficiency. What voltage and power gains expressed in dB would you expect with the load connected? If the amplifier has a peak output-current limitation of 20 mA, what is the rms value of the largest sine-wave input for which an undistorted output is possible? What is the corresponding output power available? What overall voltage gain results as measured from the source internal voltage to the load? Where did all the gain go? What would the gain be if the source was connected directly to the load? What is the ratio of these two gains? This ratio is a useful measure of the benefit the amplifier brings. Compare this value with the result in Example 1. For the design with this value of R_i , find the overall current gain and power gain. How many amplifier stages are required? What is the output voltage actually obtained? The signal source provides a mV rms signal and has a resistance of 0. Three types of voltage-amplifier stages are available: Your design should utilize the minimum number of stages and should ensure that the signal level is not reduced below 10 mV at any point in the amplifier chain. Find the load voltage and power output realized. Choose an arrangement that would cause minimum disruption to an operating circuit. Use parallel rather than series connections. The amplifiers have voltage gain, input resistance, and output resistance as follows: Your problem is to decide how the amplifiers should be connected. Find the voltage gain for each both as a ratio and in decibels. Which amplifier arrangement is best? What load voltage results? What are the corresponding voltage, current, and power gains in dB? What are the values of the open-circuit output voltage and the output resistance of the amplifier? What are the values of the short-circuit output current and the output resistance of the amplifier? Find the voltage gain realized. Sketch an appropriate amplifier topology with additional resistors selected to provide the desired result. Your design should utilize the minimum number of amplifiers and resistors. In your design, arrange to add currents. Find the input resistance R_{in} of the resulting one-port network. Apply a test voltage v_x between the two input terminals, and find the current i_x drawn from the source. Also, corresponding to a mV transducer open-circuit output voltage, the amplifier should provide a minimum of 1 V across the load. What type of amplifier is required? Sketch its circuit model, and specify the values of its parameters. Sketch its circuit model, and specify the values of the model parameters.

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PROBLEMS Circuit Basics As a review of the basics of circuit analysis and in order for the readers to gauge their preparedness for the study of electronic circuits, this section presents a number of relevant circuit analysis problems.

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